

### IN THE CLAIMS

Please cancel Claims 17, 18, and 24 without prejudice or disclaimer.

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Claim 1 (currently amended): A digital amplifier including a noise shaper and a dither generator arranged to introduce noise to the shaper, said generator using a seed value derived from a ~~state-variable~~ delay element of said shaper.

Claim 2 (original): A digital amplifier as claimed in claim 1 and wherein the number of bits in the generated noise exceeds that of the seed value.

**B** Claim 3 (original): A digital amplifier as claimed in Claim 1 or Claim 2 and wherein the dither generator includes shift registers of predetermined bit lengths to receive said seed values and provide a noise output.

Claim 4 (original): A digital amplifier as claimed in any preceding claim including means for scaling said noise.

Claims 5-16 (withdrawn)

Claims 17 and 18 (cancelled)

Claims 19-23 (withdrawn)

Claim 24 (cancelled)

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